

美國留學、職涯分享

工科14 謝靖恆

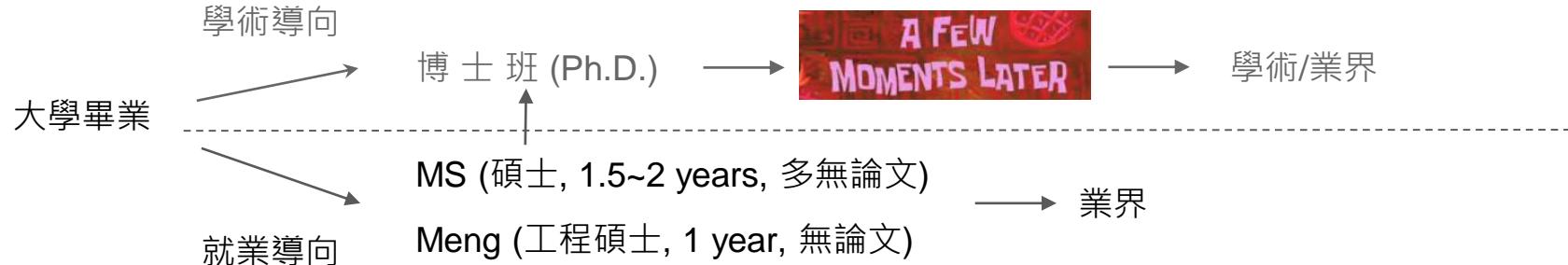
講者簡歷

2014	工科系畢業 主修電子組、物理組
2014	巫老師實驗室 研究助理
2015	UC San Diego MS, Electrical and Computer Engineering
2016	Hewlett Packard Electronics Engineering Intern
2017	Qualcomm ASIC Design Engineer

分享主題

- 碩士班定位
- 如何選校、選program
- 碩士求職過程
- 我離理想的職位有多遠？
- 移民、簽證問題
- 評估薪資待遇
- 我能開始做些什麼

美國碩士班定位



因校系而異：

Semester制 (一年兩學期) vs Quarter制 (一年三學期)

畢業考 vs 畢業論文

在美國讀碩士代價？學費生活費：粗估300萬台幣

美國碩士班課業

- 節奏較緊湊
- 評分內容包含：
作業、期中期末、lab
` final project 等
- 一學期通常修三門課
- 修課+研究+找實習/工作

難度高

Date	Lecture	Assignment
Thr 9/24	Integrated Circuit Technology	
Tue 9/29	Long Channel MOS Physics	
Thr 10/1	Common Source Amplifier, Small-Signal Model	HW 1 out
Tue 10/6	Small-Signal Model, SPICE	
Thr 10/8	Frequency Response	HW 2 out, HW 1 due
Tue 10/13	Frequency Response	
Thr 10/15	Common Gate Amplifier, Common Drain Amplifier	HW 3 out, HW 2 due
Tue 10/20	Cascodes	
Thr 10/22	Differential Amplifiers	HW 3 due
Tue 10/27	Differential Amplifiers	
Thr 10/29	Midterm	HW 4 out
Tue 11/3	Current Mirrors and Bias Network Design	
Thr 11/5	Supply Independent Biasing	HW 5 out, HW 4 due
Tue 11/10	Multistage Amplifiers	
Thr 11/12	Process Variation, Mismatch	HW 5 due, Project Out
Tue 11/17	Feedback	
Thr 11/19	Feedback	
Tue 11/24	NO CLASS (Thanksgiving Holiday)	
Thr 11/26	NO CLASS (Thanksgiving Holiday)	
Tue 12/1	Operational Transconductance Amplifiers	Project Due
Thr 12/3	Course Summary	
Wed 12/9	Final Exam (11:30 – 2:30 PM)	

\$300萬可以買到什麼？

期望	文化交流	外語學習	課程資源	研究機會	理想工作
常見困境	課業太忙、生活圈隔閡	都跟台灣or中國人討論作業	課沒開、必修衝堂、課堂額滿	教授沒錢、修課太忙	簽證問題、領域開缺少

結論：不要來美國讀碩士!!?

設法最大化碩士留學價值 - 光學位本身遠遠不足

申請留學前：

- 基本配備：**GPA (3.5+)**、托福、GRE、履歷、推薦信、SOP 等
- 選擇適合的學校、program

提前考慮碩畢後職涯發展：薪資待遇、移民簽證等等

選校、選program

名校 ≠ 職涯發展保證 排名非唯一依據

- 校系擅長領域 - 各領域教授數量
- 畢業條件 (畢業考、論文)
- 開課內容
- 研究機會、產學合作*
- 地域、學長姐畢業去處*

The screenshot shows the UC San Diego Jacobs School of Engineering website for Electrical and Computer Engineering. The top navigation bar includes links for About, People, Research, Undergraduate, Graduate, News & Events, Alumni, and Job Board, along with a search bar. The main content area has two columns: 'Graduate' on the left and 'Graduate A...' on the right, which is partially obscured by a tooltip. The 'Graduate' column lists links for Graduate Admissions, Deadlines, Requirements, and Results, Admissions FAQ, Financial Support Opportunities, SDSU / UCSD Joint Doctoral Program, Comparison of MS Degree for ECE and CSE, Comparison of PhD Degree at ECE and CSE, Current Graduate Students, Graduate Student Affairs Office, and Graduate Course Enrollment Updates. The 'Graduate A...' column lists Graduate Admissions, Current Students, Courses, and Makerspace. A tooltip for 'Graduate Admissions' contains the text: '* If you are an applicant from ecegradapps@ece.ucsd.edu'. Below the columns, it says 'ECE offers graduate programs leading to the M.S. and Ph.D. degrees, with specializations in:' followed by a list of specializations: Applied Electromagnetics, Applied Ocean Sciences, Applied Physics - Electronic Devices and Materials, Communication Theory and Systems / Magnetic Recording, Computer Engineering (Impacted Major*), Electronic Circuits and Systems (Impacted Major*), Intelligent Systems, Robotics, and Control (Impacted Major*), Machine Learning and Data Science (Impacted Major*), Medical Devices and Systems, Nanoscale Devices and Systems, Photonics, and Signal and Image Processing. At the bottom, a note states: '*You must be admitted into impacted majors directly. You may not switch into these if you are admitted into a different major.'

多數資訊各校網站都找得到 選校前要仔細閱讀

*比較難在官方公布找到訊息，各校的台灣學生會
社團是你的好夥伴

以UCSD ECE網站為例

ECE offers graduate programs leading to the M.S. and Ph.D. degrees, with specializations in:

- Applied Electromagnetics
- Applied Ocean Sciences
- Applied Physics - Electronic Devices and Materials
- Communication Theory and Systems / Magnetic Recording
- Computer Engineering (*Impacted Major**)
- Electronic Circuits and Systems (*Impacted Major**)
- Intelligent Systems, Robotics, and Control (*Impacted Major**)
- Machine Learning and Data Science (*Impacted Major**)
- Medical Devices and Systems
- Nanoscale Devices and Systems
- Photonics
- Signal and Image Processing

*You must be admitted into impacted majors directly. You may not switch into these if you are admitted.

- 組內必修規定
- 大學部、系外抵免規定
- 開課頻率

M.S. / Ph.D. Degree Planner: Applied Physics—Electronic Devices & Materials (EC76) 2019-2020

- ❖ Minimum of 12 units (Plan I) or 14 units (Plan II) must be 201+ ECE courses that must count towards your degree.
- ❖ All courses counted towards the degree must be taken for a letter grade.

Core Coursework (12 Units):

ECE 230A-B-C	Solid State Electronics I, II, III
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Any two of the following 12 unit sequences (24 units):

ECE 212AN-BN-CN*	Principles of NanoScience & NanoTechnology, NanoElectronics, NanoPhotonics
ECE 222A-B-C	Antennas & Their System Applications, Applied Electromagnetic Theory- Electromagnetics, Computational Methods for Electromagnetics
ECE 236A-B-C	III-IV Compound Semiconductor Materials, Optical Processes in Semiconductors, Heterojunction Field Effect Transistors
ECE 238A-B, MATS 201C	Thermodynamics of Solids, Solid State Diffusion & Reaction Kinetics, Phase Transformations
ECE 240A-B-C	Laser & Optics, Optical Information Processing, Optical Modulation & Detection
ECE 251A-B-C-D**	Digital Signal Processing I & II, Filter Banks & wavelets, Array Processing
ECE 260A-B-C	VLSI Digital System Algorithms & Architectures, Integrated Circuits & Systems Design, Advanced Topics
MATH 210A-B-C	Mathematical Methods in Physics & Engineering
PHYS 211A, 212A-B	Solid-State Physics I, Quantum Mechanics I & II

*Students cannot receive credit for both ECE 212A & 212AN, 212B & 212BN or 212C & 212NC

**Students may choose any three of the four courses listed for ECE 251

Technical Electives (12 Units)

- ❖ Any 4 unit, 200+ course from ECE, CSE, MAE, BENG, CENG, NANO, SE, MATS, MATH, or PHYS taken for a letter grade may be counted. Exceptions to this list require departmental approval.
- ❖ Up to 12 units of undergraduate ECE coursework (ECE 111+ only*) OR up to two 4-unit course of undergraduate ECE coursework (ECE 111+ only*) and one 4-unit course of CSE undergraduate coursework (CSE 100+ only**) may be counted.
- ❖ MS students (Plan II) are allowed no more than 4 units of ECE 299 (research units) as technical electives. PhD and MS students (Plan I) are allowed no more than 8 units of ECE 299 as technical electives.
- ❖ All courses must be taken for a letter grade.

Quarter/Core Course
ECE 230A
ECE 230B
ECE 230C
Total: 12 Units

Quarter/Add. Units
Total: 24 Units

Quarter/Tech. Electives
Total: 12 Units

以UCSD ECE網站為例 - 開課狀況

ACADEMIC YEAR: 2019-2020			
COURSE	FALL 19	WINTER 20	SPRING 20
ECE 5 Experience ECE: Making, Breaking, Hacking Stuff	NGUYEN, GILJA, HALL, ELDON	NGUYEN, GILJA, HALL	NGUYEN, GILJA, HALL
ECE 15 Engineering Computation	SCHURGERS	SCHURGERS	VARDY
ECE 16 Rapid Hardware and Software Design	KHOSHABEH	WANG	KHOSHABEH
ECE 17 Object-Oriented Programming: Design and Development with C++	GESSNER		GILJA
ECE 25 Introduction to Digital Design	QUEST	DEY	LIU
ECE 30 Introduction to Computer Engineering		YUN	MIRARAB
ECE 35 Introduction to Analog Design	SCHURGERS SCHURGERS	TAUR	SCHURGERS
ECE 45 Circuits & Systems	LUGANNANI	ZEGER	LUGANNANI
ECE 65 Components & Circuits Laboratory	BAGHDADCHI	BAGHDADCHI	BAGHDADCHI

Graduate

ECE 200. Research Conference (2)

Group discussion of research activities and progress of group members. (Consent of instructor is strongly recommended.) (S/U grades only) **Prerequisites:** graduate standing.

ECE 201. Introduction to Biophysics (4)

The class will cover fundamental physical principles of biological processes at the molecular, cellular, tissue and organ levels that are related to human physiology and diseases. Topics include energetics and dynamics of biological systems, physical factors of environment, and the kinetics of biological systems. **Prerequisites:** senior or graduate level standing.

ECE 202. Medical Devices and Interfaces (4)

This course will cover basic cellular and electrochemical processes, membrane potentials, ionic currents, nerve cell conductance, extracellular and intracellular stimulation, neural probe technology materials and devices, diagnostic and drug delivery devices, material/physiological considerations, biosensors, microfluids, optical, magnetic and electrical screening. **Prerequisites:** senior or graduate level standing.

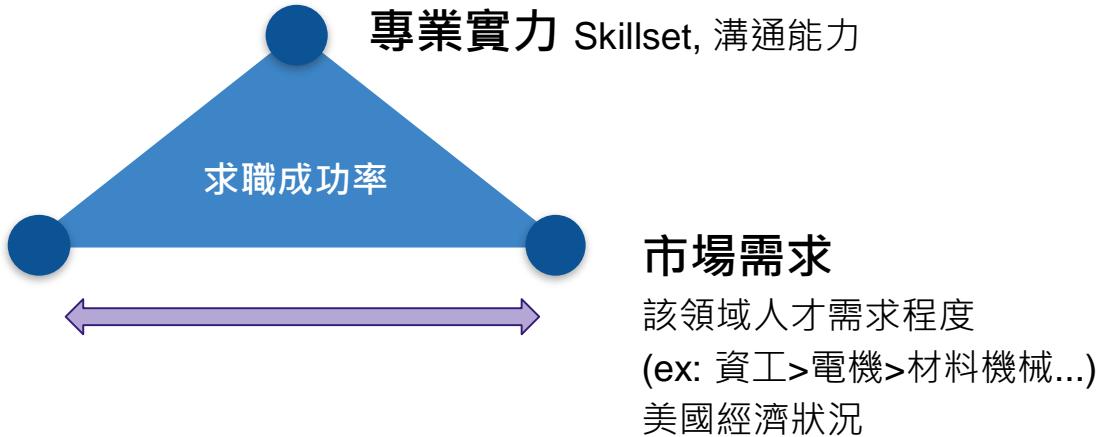
ECE 203. Biomedical Integrated Circuits and Systems (4)

Integrated circuit analysis and design for medical devices. Introduction to subthreshold conduction in MOS transistor and its similarities to biomolecular transport. Design of instrumentation amplifiers, sensors, and electrical stimulation interfaces. Transcutaneous wireless power transfer and electromagnetic effects on tissue. Recommended preparation: ECE 164 or BENG 186B or equivalent. **Prerequisites:** senior or graduate level standing.

碩士求職時程



影響求職因素



我離理想的職位有多遠？ - 職缺要求 以Intel製程工程師為例

Home > Explore Jobs > Process Engineer Phoenix, AZ

Job ID: JR0124775

Job Category: Engineering

Primary Location: Phoenix, AZ US

Other Locations:

Job Type: Experie

Job Type: Experienced Hire

Process Engineer

Job Description

Process Engineers design and plan the layout for such processes as hardening, washing, laminating, etching, engraving, polishing, painting, plating, and other material processing operations. Plans sequence of operations and specifies procedures for cutting, shaping, and otherwise preparing basic material, exercising judgment in compromising between conflicting requirements, economic evaluation of methods, and operator effectiveness and comfort. Conducts tests and measurements throughout stages of production to determine control over such variables as temperature, density, specific gravity, pressure, and viscosity. Establishes and submits processing requirements to be met in designing and acquiring processing equipment. Responds to customer/client requests or events as they occur. Develops solutions to problems utilizing

Minimum Requirements:

Qualific

You must
school w

Minimur

- Master's or Ph.D's Degree in Materials Science, Electrical Engineering, Mechanical Engineering, Optical Engineering, Chemical Engineering, Chemistry, Applied Mechanics, or Physics.
- 5+ years experience in CMP, preferably with low K dielectric polish and metals polish.

Preferred Qualifications:

Preferre

- Experience with:
- Data analysis (JMP, SPC, MATLAB, Mathematica)
- Design of experiment (DOE) principles
- Semiconductor processes (Thin Films, Diffusion, Planar, Implant, Litho, Etch, and/or Metrology)
- Understanding the relationship between processing versus microstructure versus mechanical and physical properties of materials (polymers, composites, metals, and ceramics)
- Working with fracture and adhesion mechanisms of materials and material interfaces



能否於校內或實習獲得這些技能？

我離理想的職位有多遠？ - 自我條件

- 參考前人經驗、經歷
- 相關技能能否藉由學位、實習獲得
- LinkedIn: 專業版的臉書
 - 建立線上履歷
 - 尋找職缺
 - 聯絡人資

Jiang's LinkedIn Profile Summary:

- Education:** University of Connecticut (Doctor of Philosophy - PhD, Chemical engineering, 2009 – 2014); Dalian University of Technology (Bachelor's degree, Chemical engineering, 2004 – 2009)
- Experience:**
 - Process Engineer** at Intel Corporation (Mar 2019 – Present, 10 mos, Chandler, AZ)
 - Senior Process Engineer** at ASM (Dec 2014 – Mar 2019, 4 yrs 4 mos, Phoenix, Arizona Area)
 - Led novel thin film ALD dielectric processes development for Logic and DRAM applications based on customers' requested film properties and specifications.
 - Developed/produced an ALD process for DRAM high-K layer; Identified process sensitivity tuning knobs for film thickness and uniformity via JMP DOE; established process stability ...[see more](#)
 - Graduate Research Assistant** at University of Connecticut (Aug 2009 – Nov 2014, 5 yrs 4 mos, Storrs, CT)
 - Selective ALD copper deposition
 - Fundamentally studied selective copper ALD on palladium by real-time spectroscopy ellipsometry (RTSE).
 - Proposed a new mechanism involving reversible and dissociative adsorption of the pre ...[see more](#)
 - Lab user** at CNS at Harvard (2010 – 2014, 4 yrs)
 - Deposition processes with CVD/ALD/PVD for various projects

Education

- University of Connecticut
Doctor of Philosophy - PhD, Chemical engineering
2009 – 2014
- Dalian University of Technology
Bachelor's degree, Chemical engineering
2004 – 2009

Skills & Endorsements

Characterization · 9

Intel Corporation Endorsed by 2 of Jiang's colleagues at Intel Corporation

Powder X-ray Diffraction · 7

ASM Endorsed by 2 of Jiang's colleagues at ASM

XPS · 5

ASM Endorsed by 2 of Jiang's colleagues at ASM

Industry Knowledge

Scanning Electron Microscopy · 5	Spectroscopy · 4
Nanomaterials · 4	Thin Films · 4

Other Skills · 0

AFM · 5	CVD · 5
Atomic Layer Deposition · 4	Surface Chemistry · 4
Photolithography · 3	Electron Beam Lithography · 3
CMOS · 3	UHV · 2
Sputtering · 2	Ellipsometry · 2
XRR · 2	SIMS · 1

碩士求職時程



面試流程 - 以高通硬體工程師為例



履歷篩選

英文履歷，由人資、主管挑選
(學期初career fair、內推、海投)

Phone Interview

1~2輪
1小時電話面試篩選
基本專業知識、提問過去經驗

On-site Interview

1~2輪
全天面試 一小時一輪 共5~8輪
白板解題、討論溝通、專業知識

Offer

人資、主管、經理討論offer

移民、工作簽證*

		內容	不確定因素
非移民傾向	F1 - CPT 學生簽證 - 實習工作許可	就學滿一年 實習與所學相關	與所學相關定義
非移民傾向	F1 - OPT 學生簽證 - 畢業工作許可	完成學位可取得 (最多畢業後5個月內須找到工作) 工作與所學相關 一年、STEM領域可延長兩年	與所學相關定義 90天失業期
Dual intent	H-1B 工作簽證	需雇主贊助 一次三年，可延長一次 若雇主幫助申請綠卡，可持續延長	須抽籤，單次50%+ 60天失業期
移民傾向	綠卡 永久居民	需雇主贊助 PERM, I-140, I-485 台灣人約3年	S.386法案

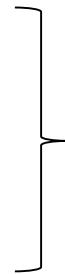
*詳細規定以官方公布為準

薪水多少合理？work life balance?

聽鄉民說 5萬 10萬 20萬 50萬 代表什麼？

薪資組成：

- Base salary,** — 底薪
- Sign-on bonus, — 簽約金
- RSU, — 股票(分年領)
- Annual bonus. — 年終



Total Compensation (TC)

薪水行情參考：

Glassdoor - <https://www.glassdoor.com/>

h1b salary database - <https://h1bdata.info/>

Glassdoor -

<https://www.glassdoor.com/>

The screenshot shows the Glassdoor company profile for Intel Corporation. At the top, there's a banner with the Intel logo and several small images of people working. Below the banner, the company name "Intel Corporation" is displayed along with its status as an "Engaged Employer". Key metrics shown include 14k reviews, 2.0k jobs, 26k salaries, 3.9k interviews, 5.4k benefits, and 264 photos. There are "Follow" and "Add a Salary" buttons.

Intel Corporation Engineering Salaries
22,416 salaries (for 2,068 job titles) Updated Dec 14, 2019

22,416 Intel Corporation employees have shared their salaries on Glassdoor. Select your job title and find out how much you could make at Intel Corporation.

A search bar for "Search job titles" and a "Find Salaries" button are present. Filter options include "Clear All", "Engineering", "United States - All Cities", and a "Filter" button. A sorting dropdown shows "Sort: Most Reports".

Average Base Salaries in (USD) Low High

Job Title	Average Base Salary
Software Engineer	\$102,416/yr
Electronics Engineer	\$111,163/yr
Process Engineer	\$108,316/yr

Intel Corporation Careers

Come Build the Future with Us At Intel, we develop technologies that break barriers and enable tomorrow's greatest scientific and cultural... - More

- A. I.
- Automotive
- Memory
- Students

Jobs You May Like

Process Technology Development Engineer
Intel - Hillsboro, OR

h1b salary database -

<https://h1bdata.info/>

The screenshot shows the h1b salary database search interface. It features a search bar with fields for "Intel Corporation", "process engineer", "and/or City", and "2019". There are "Search" and "Auto Replay" buttons. A note says "New Click to see up to date H1-B filings from Intel Corporation".

106 Records, Median Salary \$95575

Employers (1) - Job Titles (1) - Cities (8) - Years (1) -

<100k		100k-150k				
EMPLOYER	JOB TITLE	BASE SALARY	LOCATION	SUBMIT DATE	START DATE	CASE STATUS
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	01/28/2019	07/22/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/19/2019	08/15/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/22/2019	08/20/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/26/2019	08/21/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/26/2019	08/22/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/27/2019	08/23/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	02/28/2019	08/27/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	03/04/2019	08/30/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	03/07/2019	09/05/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	03/15/2019	09/13/2019	CERTIFIED
INTEL CORPORATION	PROCESS ENGINEER	74,485	CHANDLER, AZ	03/16/2019	09/14/2019	CERTIFIED

關於所得稅

關於稅：聯邦稅+州稅 (可以30%估計)

<https://www.irscalculators.com/international-tax-calculator> (僅供參考)

Country: Taiwan 

Taxable Income: NTS 1,200,000

Currency: (TWD) New Taiwan Dollar

Search countries whose highest tax bracket is between
0 and 1 for individuals with domestic income

Search

Domestic Person

If Taxable Income is Between		Rate
NT\$0	and	NT\$520,000
NT\$520,000	and	NT\$1,170,000
NT\$1,170,000	and	NT\$2,350,000
NT\$2,350,000	and	NT\$4,400,000
NT\$4,400,000	and	NT\$10,000,000
NT\$10,000,000	and	Higher

Tax Amount: NT\$ 107,600 Marginal Rate 12%

Your Share: NT\$ 1,092,4 Average Rate: 8.97%

Country: Germany 

Taxable Income: € 89,920

Currency: (EUR) Euro

Search countries whose highest tax bracket is between
0 and 1 for individuals with domestic income

Search

Domestic Person

If Taxable Income is Between		Rate
€0	and	€8,355
€8,355	and	€52,882
€52,882	and	€250,731
€250,731	and	Higher

Tax Amount: € 21,790 Marginal Rate 42%

Your Share: € 68,130 Average Rate: 24.23%

Country: United States of Amer 

Taxable Income: \$ 100,000

Currency: (USD) United States Dollar

Search countries whose highest tax bracket is between
0 and 1 for individuals with domestic income

Search

Domestic Person

If Taxable Income is Between		Rate
\$0	and	\$9,225
\$9,225	and	\$37,450
\$37,450	and	\$90,750
\$90,750	and	\$189,300
\$189,300	and	\$411,500
\$411,500	and	\$413,200
\$413,200	and	Higher

Tax Amount: \$ 20,794 Marginal Rate 25%

Your Share: \$ 79,206 Average Rate: 20.79%

未計入州稅

關於物價 Cost of living

矽谷\$1 ≠ 休士頓\$1

Cost of living in San Jose, California, United States vs Houston, Texas, United States

How much money will you need in San Jose, California?
Find out with your own [Salary Calculation](#).

Currency:	Other currency
Food	+ 26%
Housing	+ 63%
Clothes	+ 36%
Transportation	+ 21%
Personal Care	+ 19%
Entertainment	+ 22%
TOTAL	+ 36%

Cost of living in San Jose, California (United States) is 36% more expensive than in Houston, Texas (United States)

需依照個人需求、生活方式評估

Cost of living in San Jose, California, United States vs Hsinchu City, Taiwan

Food	+ 56%
Housing	+ 198%
Clothes	+ 56%
Transportation	+ 43%
Personal Care	+ 75%
Entertainment	+ 69%
TOTAL	+ 90%

Cost of living in San Jose, California (United States) is 90% more expensive than in Hsinchu City (Taiwan)

[問卦] 台灣 vs 美國，東岸 vs 西岸，亞洲 vs 歐洲？

客觀條件：

工作機會、薪水、稅率、物價

主觀條件：

生活方式、飲食習慣、家人、伴侶等

正確運用人脈 - 獲得第一手資訊

詢問個別校系、工作職缺問題:

Ex: 特定校系學生找工作狀況

推薦課程

特定研究室風氣

留學生活撇步

內推 (Internal Referral) :

最有效的求職方式

認識新朋友

- 如何與陌生人開啟對話

1. 概略自我介紹和目的

2. 詳述問題

- 我想獲得什麼幫助

- 我的目標

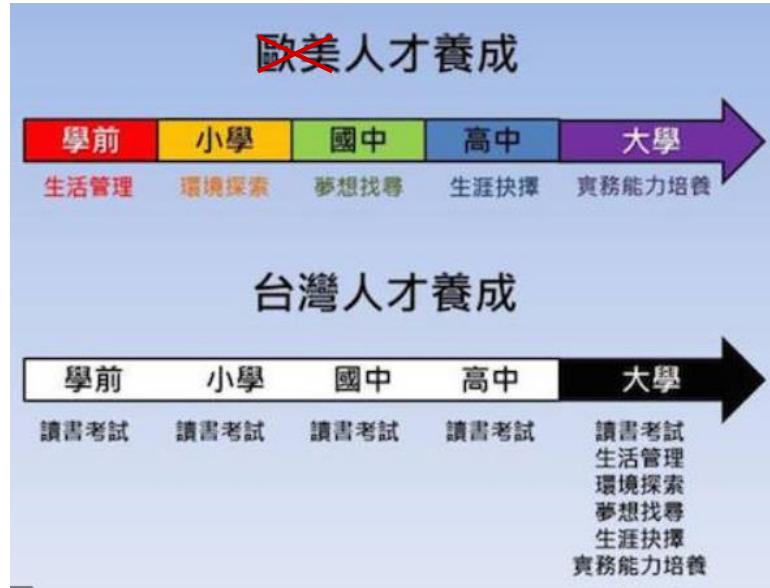
- 對於這個目標我有什麼基礎

3. 適當追問

4. 結束對話

5. 回報後續 - 表示重視,維持連結

我能開始做些什麼？ - 不論要不要留學



顧好GPA=顧好身體

英文能力、表達能力

- 語言中心英文課
- 上台報告、課外活動
- 專業文獻: 原文書paper, 英文維基百科
- 英文融入生活 ex: 英文字幕美劇、YT、英文球評NBA

知道自己想做什麼、能做什麼

- 專題實作
- 企業實習
- 不為考試而學習、不被人情綁架參與活動

選你所愛，愛你所選



United States
Q & A

Gulf of California

Mexico

Gulf of Mexico

Cuba

Puerto Rico

Dominican Republic